


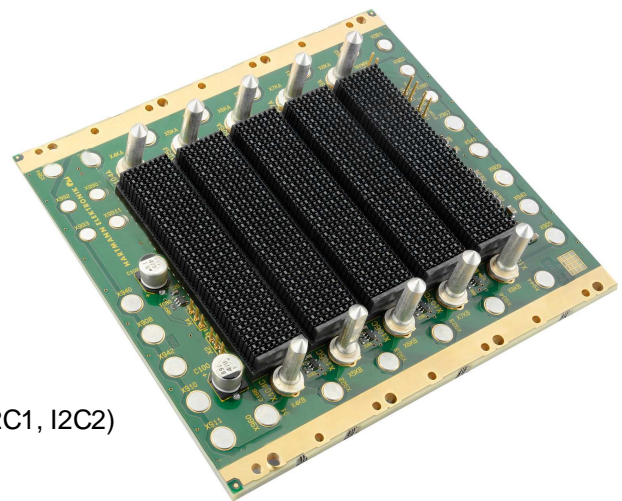
3U VPX BACKPLANE	VITA 46	

Key figures:

- Compliant to VITA 46.0 baseline specification
- Supports VITA 46.3 Serial Rapid I/O
- Supports VITA 46.4 PCI Express
- Supports VITA 46.7 Ethernet
- 3U, 5 Slot, Full Mesh X4 configuration
- M4 screws for powerentry
- PCB size 128.5mm x 142.9mm x 4.3 mm
- 4 HP from slot to slot (20.32mm)
- System Management Interface on the backplane (I2C1, I2C2)
- Flexible keying and alignment mechanism
- with JTAG connector on first and last slot
- with geographical address pins
- Reference clock
- Non-Volatile Memory Read Only signal set by Jumper BR1
- Battery backup option setting by Jumper X5. Vbat external or connected to 3.3 AUX.
- System Reset
- P1 Reserved Signals
- Operating temperature: -40° - +85°C
- Storage temperature: -55°C - +85°C
- Flammability rating: UL94-V0

- Partnumber: B190050010

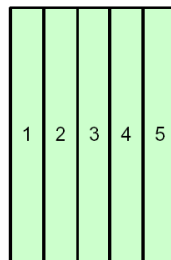
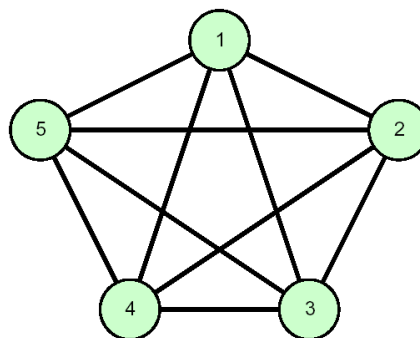
- 6 Slot VPX Backplane version partnumber: B190060010



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VITA 46

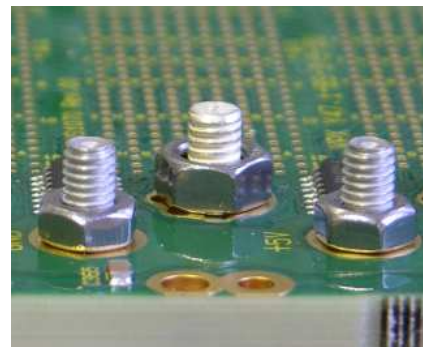

1) Topology: Full Mesh X4

„Fat pipes“ from Slot to Slot, that means 4 lanes/ link (= 8 differential pairs)



2) Power Studs M4

The main operating voltages (+12V, +3.3V, +5V) and GND are supplied with M4 screw terminals. The auxiliary operating voltages are supplied via M3 screw terminals. Optimal daughter board supply and trouble-free operation are ensured by the arrangement of the feed modules on the backplane.



The permissible max. current are 40 A/ M4 screw

Consider: Max. 36A/ Slot acc. VITA 46.0 allowed

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3) Utility connector

There are 2 connectors for system-management I2C1 and I2C2. I2C1_PWR and I2C2_PWR are connectable to power with 3-pin feed-through connectors.

For customer specific board assembly are Zero-Ohm resistors available.

Usable voltages are 5V / 3.3V-AUX.

I2C1

1	I2C1_SCL
2	GND
3	I2C1_SDA
4	I2C1_PWR
5	NC

I2C2

1	I2C2_SCL
2	GND
3	I2C2_SDA
4	I2C2_PWR
5	NC

I2C1-PWR

1	+5V
2	I2C1_PWR
3	+3.3V_AUX

I2C2-PWR

1	+5V
2	I2C2_PWR
3	+3.3V_AUX

4) JTAG connector

In addition 2 JTAG-Connectors for VPX-Slots 1 (X200) and 5 (X201) are available.

JTAG

1	GND
2	TCK
3	TMS
4	TRST*
5	TDI
6	TDO

5) VBAT X5

Normally a battery voltage with approximately 3V is available at Pin VBAT of connector VPX-J1. The voltage is externally accessible with connector X5 or internally using 3.3V_AUX by closing Jumper BR2.

VBAT X5

1	GND
2	+3V Batterie

BR2

1	+3.3V_AUX
2	+VBAT

6) NVMRO

If Jumper BR1 is closed NVRMO is set to memory writeable.

BR1

1	NVMRO
2	GND

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